



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

Currie et al.

CONFIRMATION NO.: 7878

SERIAL NO.:

10/696,994

GROUP NO.:

2823

FILING DATE:

October 30, 2003

EXAMINER:

Brewster, W.

TITLE:

METHODS FOR PRESERVING STRAINED SEMICONDUCTOR

SUBSTRATE LAYERS DURING CMOS PROCESSING

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO SPECIES ELECTION REQUIREMENT

Sir:

This paper responds to the Office Action mailed from the U.S. Patent and Trademark Office on January 13, 2005, in connection with the patent application identified above. Applicants enclose a Petition for Extension of Time for one month and the appropriate fee. Applicants believe that no other fees are required. However, please charge any other fees necessary for entry of this paper to Attorney's Deposit Account No. 01-1700.

Remarks begin on page 2.